

118

EE318061799US

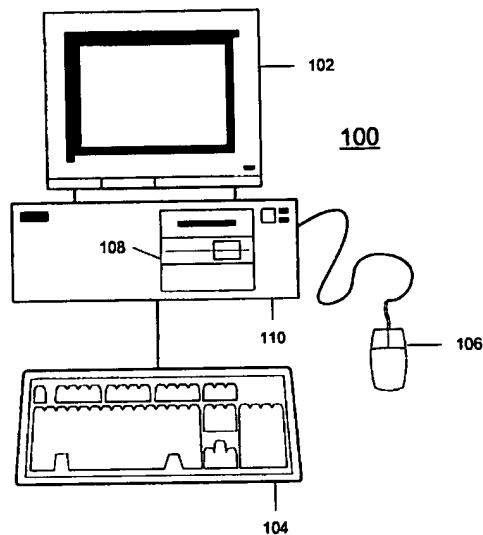


Figure 1  
AT9-89-073

09110912-051399

218

09310912-051399

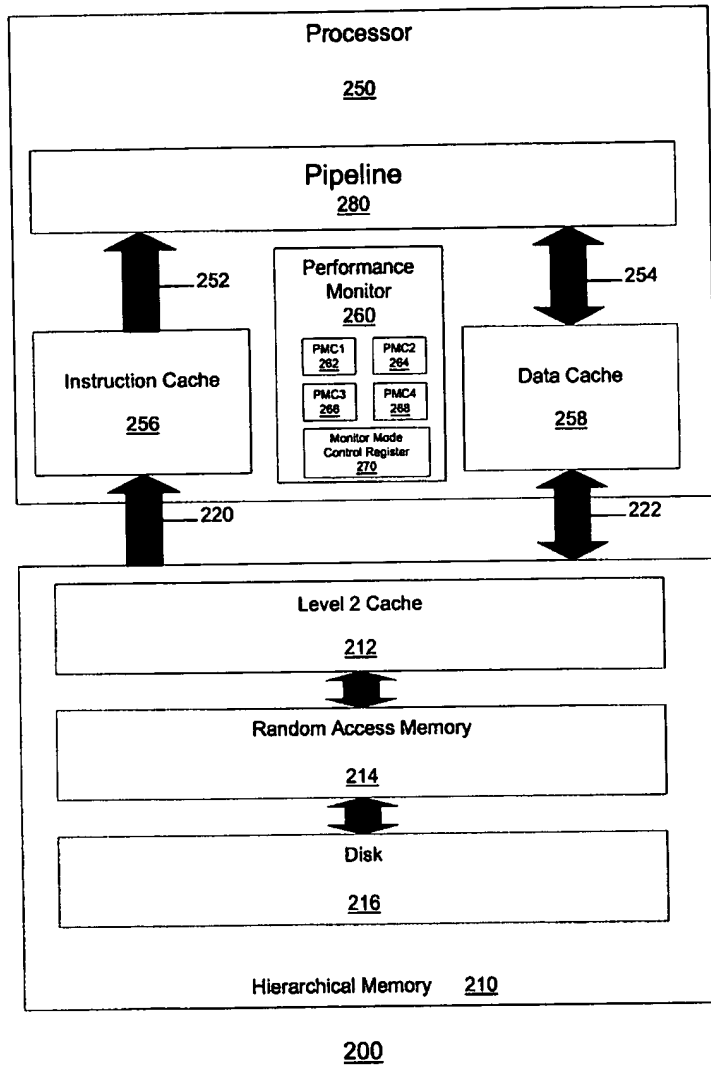


Figure 2

AT9-99-073

318

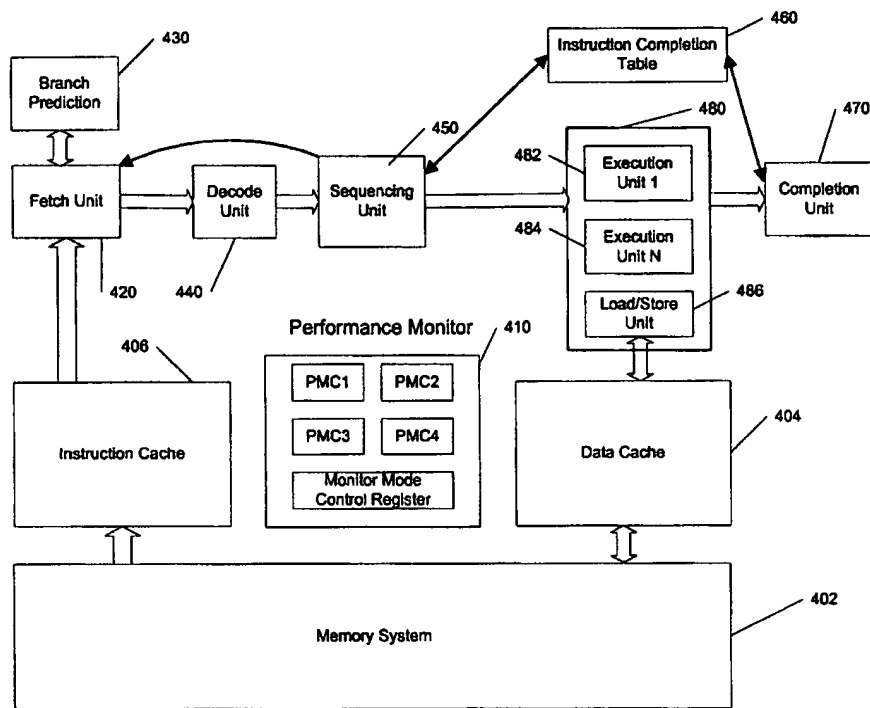
666150"21601650

Bits 0-4 Counting Enables	Bit 5 Interrupt Enable	Bits 6-15	Bit 16 PMC2 Interrupt Control	Bit 17 PMC2 Interrupt Control	Bit 18 PMC2 Count Control	Bits 19-25 PMC1 Event Selection	Bits 26-31 PMC2 Event Selection
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Monitor Mode Control Register  
(MMCR)

Figure 3  
AT9-99-073

418

400**Figure 4**

AT9-99-073

518

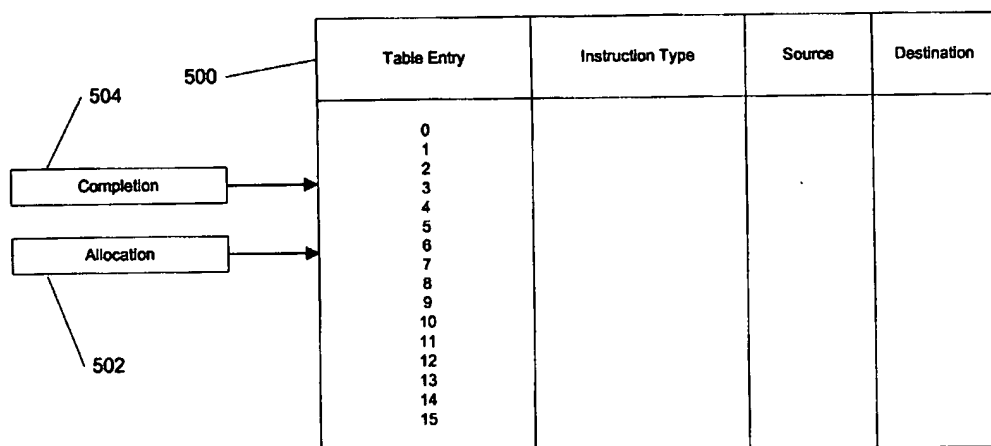


Figure 5

AT9-99-073

678

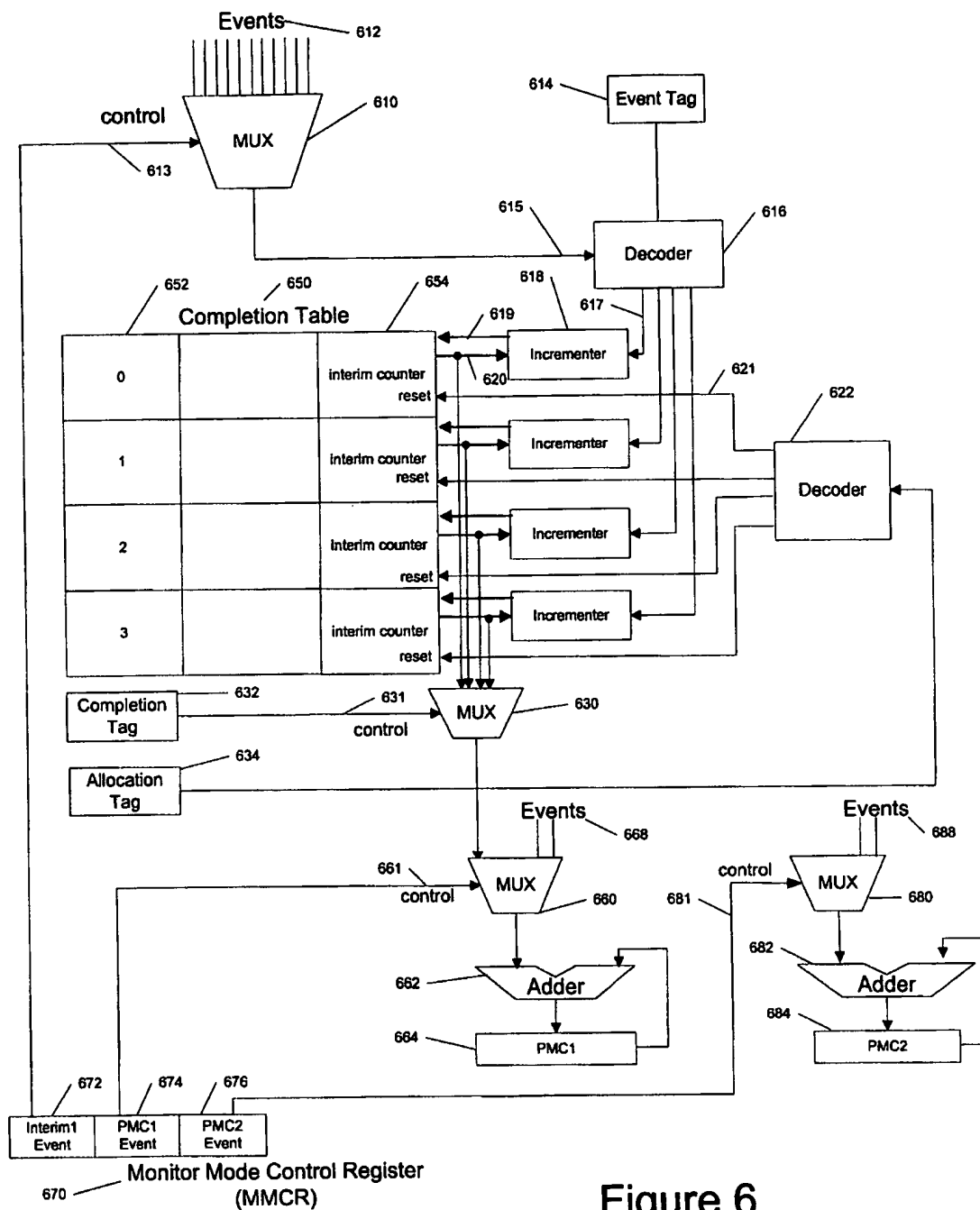


Figure 6

AT9-99-073

718

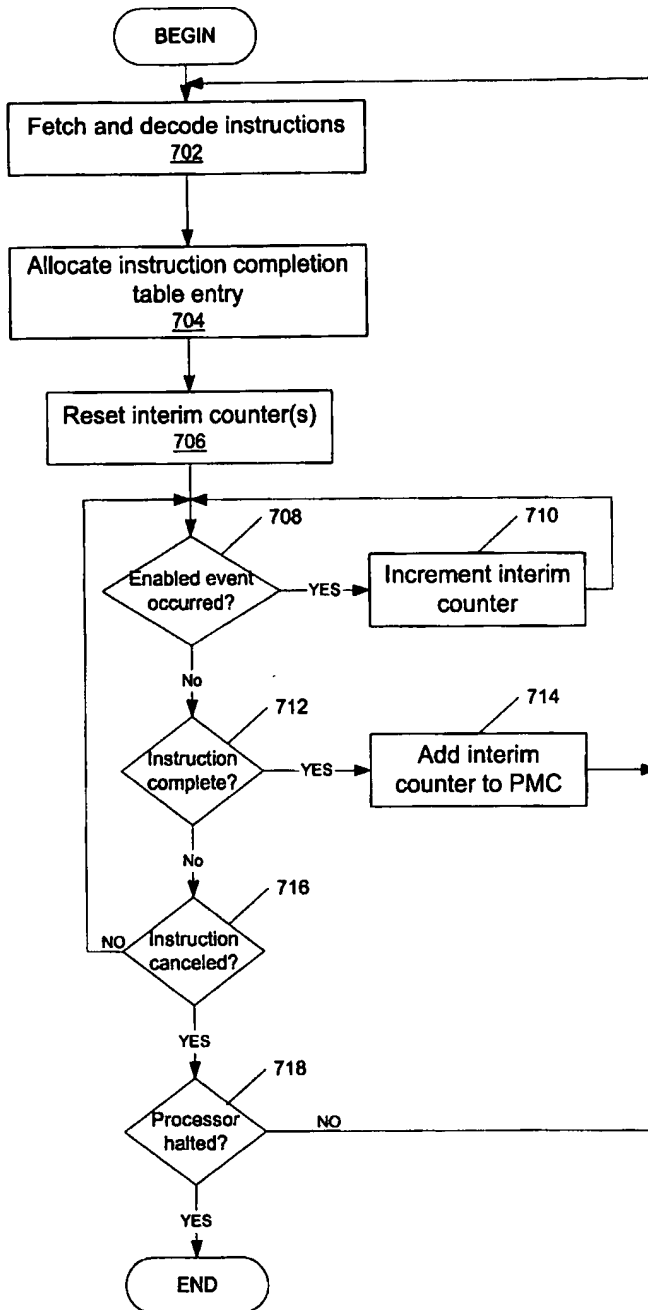
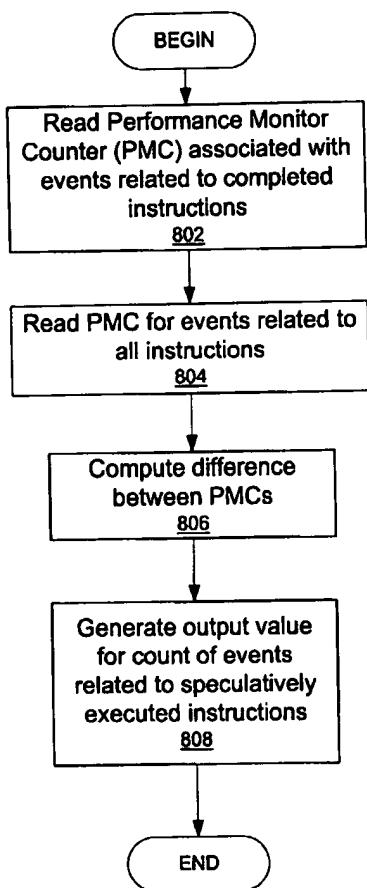


Figure 7

AT9-99-073

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### Figure 8

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